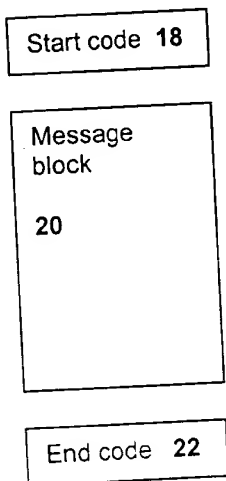


**Figure 1**

**24A**  
Longer Message



**24B**  
Shorter Message

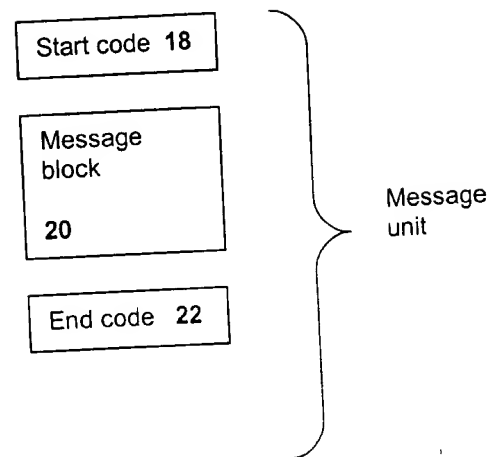


Figure 2

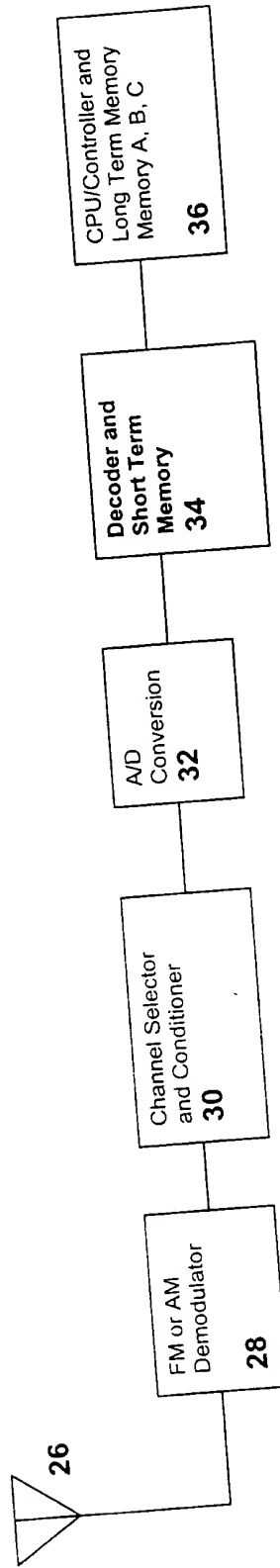


Figure 3

FIG. 4 is a block diagram of a system 42 in accordance with the present invention. The system 42 includes a Sub-carrier Receiver Module 38, a Disc Player Module 40, a Power Supply 44, a Sub-carrier/Disc Player Interface Control Circuitry 54, a DAC/Audio Synthesizer 46, a User Interface 48, a Display 50, and an Audio Output 52. The Sub-carrier Receiver Module 38 is connected to an antenna 26 and the Sub-carrier/Disc Player Interface Control Circuitry 54 via an I/O interface. The Disc Player Module 40 is connected to the Sub-carrier/Disc Player Interface Control Circuitry 54 via an I/O interface. The Power Supply 44 is connected to both the Sub-carrier Receiver Module 38 and the Disc Player Module 40. The Sub-carrier/Disc Player Interface Control Circuitry 54 is connected to the DAC/Audio Synthesizer 46, the User Interface 48, the Display 50, and the Audio Output 52.

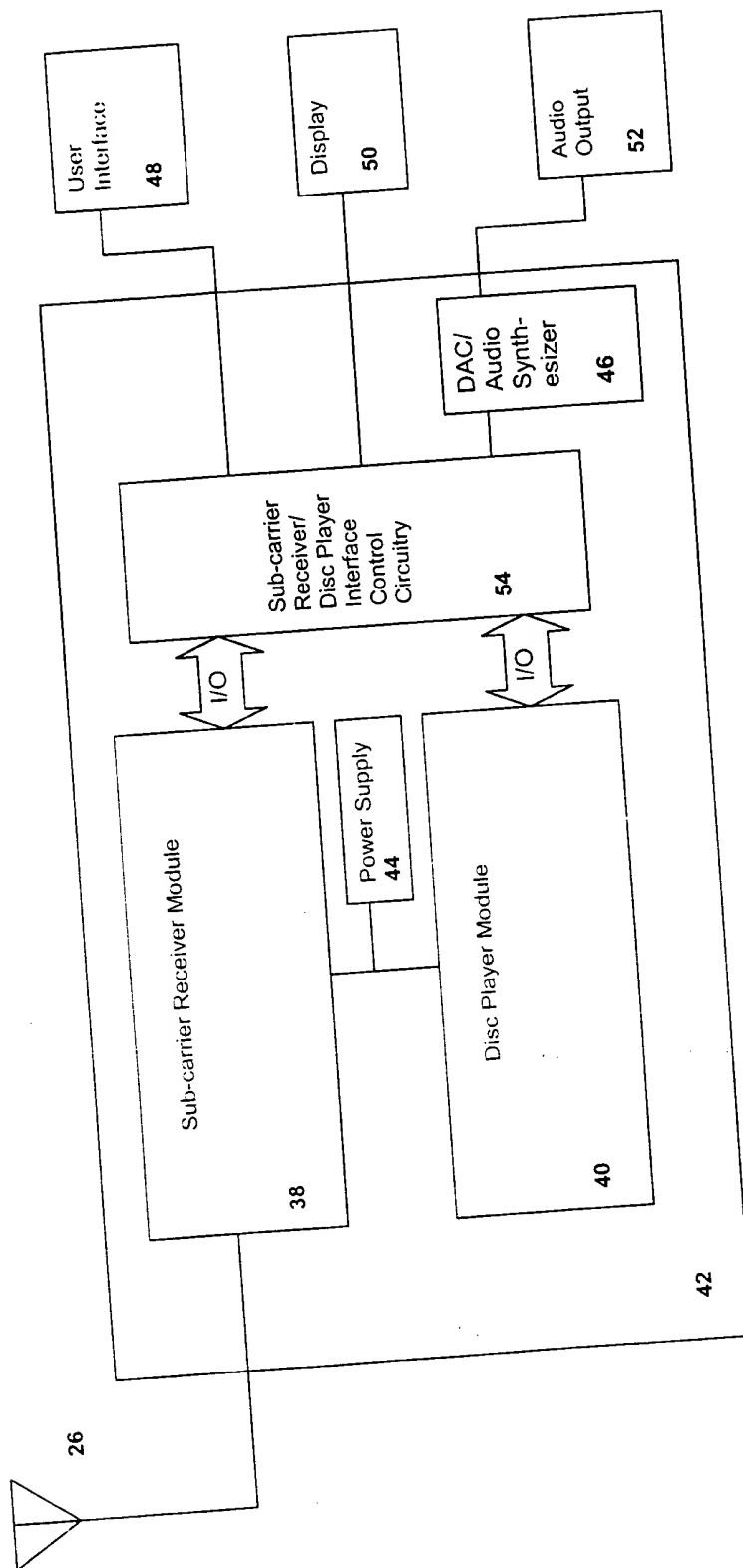


Figure 4

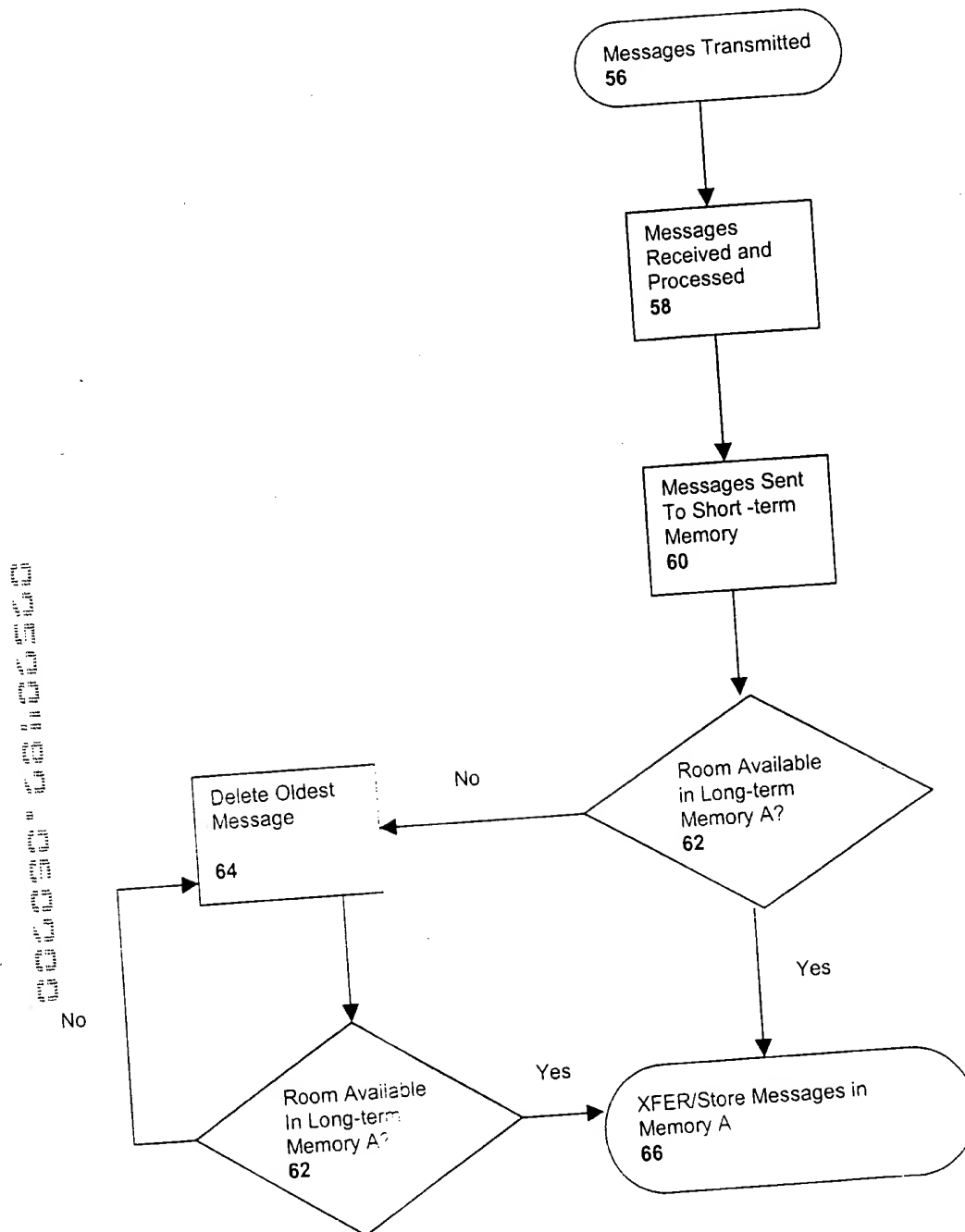


Figure 5

68 69 70 71 72 73 74 75 76 77 78 79 80 81

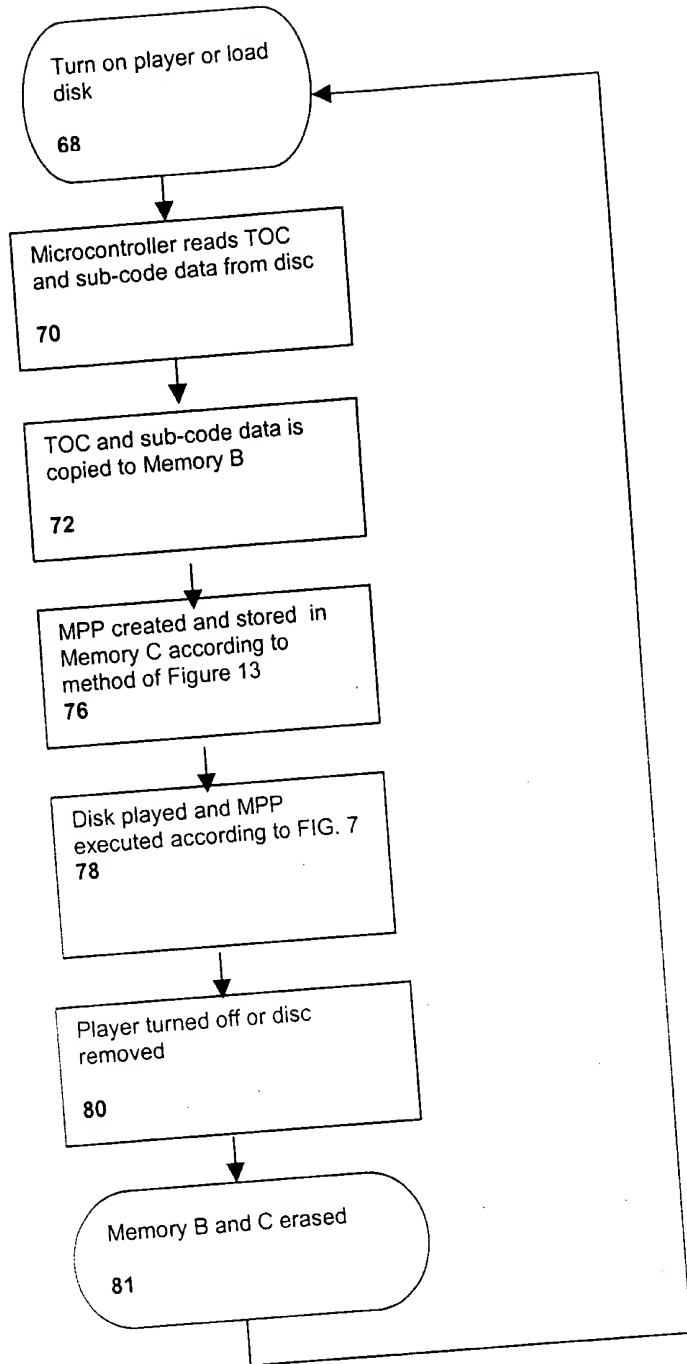
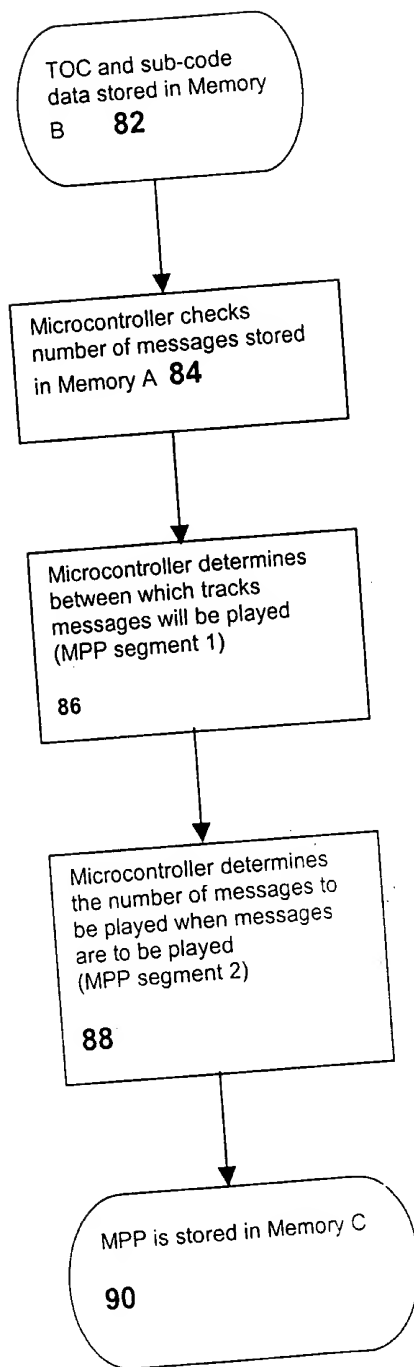


Figure 6



**Figure 7**

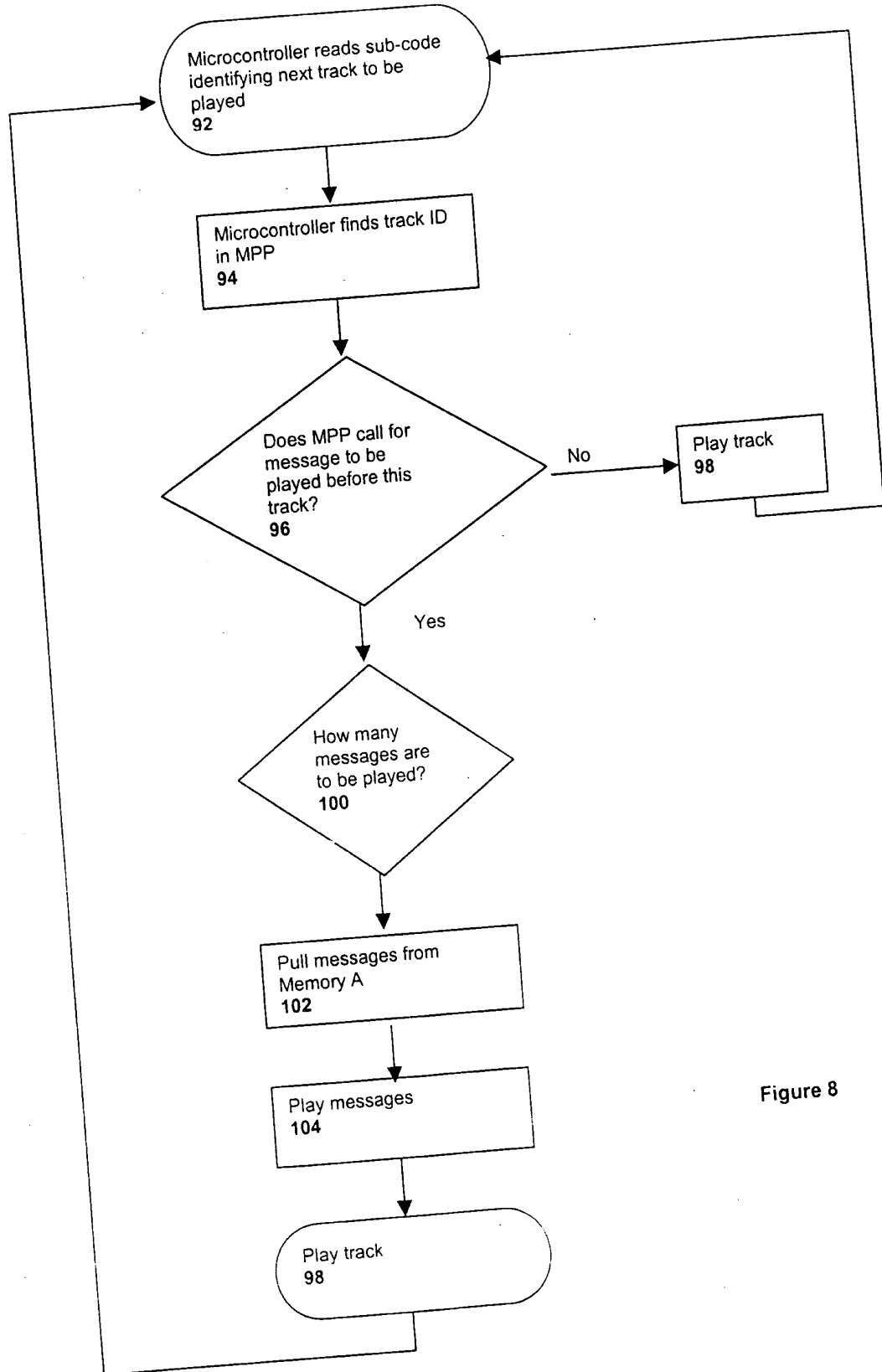


Figure 8